

FAST - (Untitled:1)

Drafts

- IS&R
- Pending
- Active
  - L2: (79) differential adj. amplifier and folded adj. cascode adj. amplif.
  - L3: (74) 2 and bias
  - L4: (41) 3 and current adj. mirror
- Failed
- Saved
- Favorites
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- UDC
- Queue
- Trash

DBA USPCB USPAT USOCREF ID DERVENT G116

Default operator: OR

3 and current adj. mirror

GRSTerm SMTerm Image PDF HTML

U	Document ID	Issue Date	SParts	Title	Current DR	Current XRef	Retrieval Details	Inventor	IS	PC	PA	PN
1	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20040201416 A1	20041014	26	On-chip calibrated source termination for voltage mode driver and method of calibration thereof	330/2			Wyers, Eric James et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/> <input checked="" type="checkbox"/> US 2004090268 A1	20040513	17	Amplifier gain boost circuitry and method	330/253			Sanchez, Stephen J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20040008096 A1	20040115	17	AMPLIFIER GAIN BOOST CIRCUITRY AND METHOD	330/260			Sanchez, Stephen J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20030090321 A1	20030515	14	Bipolar class AB folded cascode operational amplifier for high-speed applications	330/252			Parkhurst, Charles et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20030020518 A1	20030130	15	Output driver for high speed Ethernet transceiver	327/106	326/82		Barvel, Nicholas van et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20020135396 A1	20020926	14	Circuit for receiving and driving a clock signal	326/17			Kiehl, Oliver	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/> <input checked="" type="checkbox"/> US 20020109532 A1	20020815	7	Comparator circuit	327/74			Nakada, Akira	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6803655 B1	20041012	41	Two-stage auto-zero amplifier circuit for electro-optical arrays	250/214G; 330/309			Parish, William J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6781450 B1	20040624	12	Low-frequency auto-zeroed amplifier	330/9	330/69		Mercer, Mark J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6710654 B2	20040323	10	Bipolar class AB folded cascode operational amplifier for high-speed applications	330/252	330/250		Parkhurst, Charles et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/> <input checked="" type="checkbox"/> US 6665347 B2	20031216	11	Output driver for high speed Ethernet transceiver	375/257	326/30; 326/82; 327/156		van Barvel, Nicholas et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>